

Team Name: sdmay24-40

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Report Period: Oct 22-Nov 5

Summary of Progress in this Period

RF board was ordered and arrived. RF board is assembled. ADC board schematic is in progress. FPGA programming started.

Pending Issues

ADC schematic is not completed.

No testing on the RF board.

Interfacing between the FPGA and the computer is not done.

Plans for Upcoming Reporting Period

ADC board is done and ordered.

Testing on RF board completed.

Interfacing between the FPGA and computer is done.
